

**UNIVERSITY COLLEGE OF ENGINEERING
OSMANIA UNIVERSITY, HYDERABAD - 7**

**M. E. (ECE, Mechanical & CSE) II - Semesters (Main) Examination
September 2015**

**M V S R ENGINEERING COLLEGE
EXAMINATION TIME-TABLE**

Time : 2.00 PM to 5.00 PM

DATE & DAY	E. C. E. (EMBEDDED SYSTEMS & VLSI DESIGN)	MECHANICAL (CAD / CAM)	C. S. E.
	II-Semester	II-Semester	II-Semester
14-09-2015 MONDAY	Scripting Languages for VLSI Design Automation	Non-Traditional Machining and Forming	=====
16-09-2015 WEDNESDAY	=====	Computer Aided Mechanical Design & Analysis	Advanced Databases
18-09-2015 FRIDAY	Low Power VLSI Design	Automation	Human Computer Interaction
21-09-2015 MONDAY	Real Time Operating Systems	Rapid Prototyping Principles & Applications	Distributed Computing
23-09-2015 WEDNESDAY	VLSI Physical Design	Failure Analysis & Design	Machine Learning
25-09-2015 FRIDAY	DSP Processors Architecture	Programming Methodology & Data Structures	Image Processing
28-09-2015 MONDAY	Advanced Computer Organization	Optimization Techniques	=====
30-09-2015 WEDNESDAY	=====	Flexible Manufacturing Systems	Network Security

Note: This Time-Table is also applicable for Pre-Ph.D. students.


**DIRECTOR OF EVALUATION
EXAMINATION CELL**